

Computer Science CSCI 355

Digital Logic and Computer Organization

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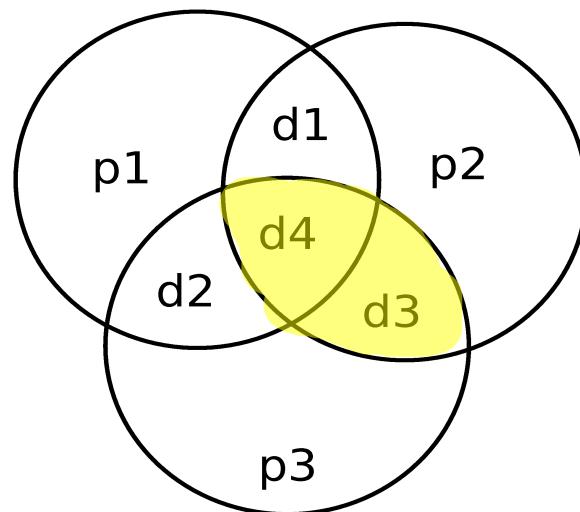
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Hamming Code

- $n = 4 \ m = 3$



e.g. $c_3 = 1, c_2 = 1, c_1 = 0$
 d_3 is in error

Hamming Code cont.

Parity bits cover sequences of data bits.

- $p_1 = d_1 \oplus d_2 \oplus d_4$
- $p_2 = d_1 \oplus d_3 \oplus d_4$
- $p_3 = d_2 \oplus d_3 \oplus d_4$

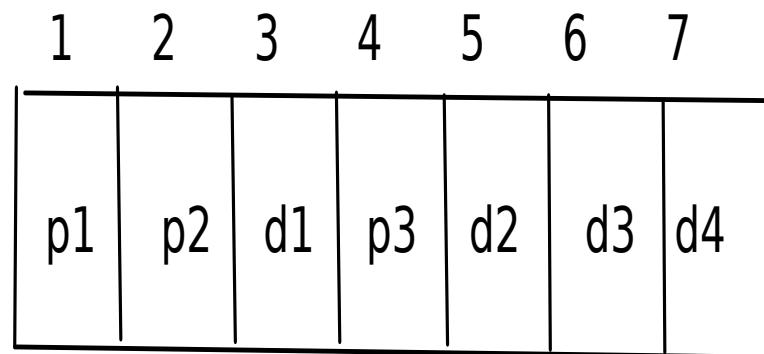
No data bits are covered by the same sequence of parity bits.

- d_1 is covered by p_1, p_2
- d_2 is covered by p_1, p_3
- d_3 is covered by p_2, p_3
- d_4 is covered by p_1, p_2, p_3

Hamming Code cont.

Syndrome			Inference
c_3	c_2	c_1	
0	0	0	no error
0	0	1	p1 in error
0	1	0	p2 in error
0	1	1	d1 in error
1	0	0	p3 in error
1	0	1	d2 in error
1	1	0	d3 in error
1	1	1	d4 in error

Transmit Bit Ordering



Example

- $d_1, d_2, d_3, d_4 = 0110$

1	2	3	4	5	6	7
p1	p2	d1	p3	d2	d3	d4
?	?	0	?	1	1	0

Example cont.

- $p1 = d1 \oplus d2 \oplus d4$
 $\Rightarrow p1 = 0 \oplus 1 \oplus 0 = 1$
- $p2 = d1 \oplus d3 \oplus d4$
 $\Rightarrow p2 = 0 \oplus 1 \oplus 0 = 1$
- $p3 = d2 \oplus d3 \oplus d4$
 $\Rightarrow p3 = 1 \oplus 1 \oplus 0 = 0$

Example cont.

○ Transmit

1	2	3	4	5	6	7
p1	p2	d1	p3	d2	d3	d4
1	1	0	0	1	1	0

Example cont.

- Receive

1	2	3	4	5	6	7
p1	p2	d1	p3	d2	d3	d4
1	1	0	0	1	1	0

Example cont.

- $c_1 = p_1 \oplus d_1 \oplus d_2 \oplus d_4$
 $\Rightarrow c_1 = 1 \oplus 0 \oplus 1 \oplus 0 = 0$
- $c_2 = p_2 \oplus d_1 \oplus d_3 \oplus d_4$
 $\Rightarrow c_2 = 1 \oplus 0 \oplus 1 \oplus 0 = 0$
- $c_3 = p_3 \oplus d_2 \oplus d_3 \oplus d_4$
 $\Rightarrow c_3 = 0 \oplus 1 \oplus 1 \oplus 0 = 0$
- Syndrome = 000 \Rightarrow no bits in error

Example cont.

- Receive

1	2	3	4	5	6	7
p1	p2	d1	p3	d2	d3	d4
1	1	1	0	1	1	0

Example cont.

- $c_1 = p_1 \oplus d_1 \oplus d_2 \oplus d_4$
 $\Rightarrow c_1 = 1 \oplus 1 \oplus 1 \oplus 0 = 1$
- $c_2 = p_2 \oplus d_1 \oplus d_3 \oplus d_4$
 $\Rightarrow c_2 = 1 \oplus 1 \oplus 1 \oplus 0 = 1$
- $c_3 = p_3 \oplus d_2 \oplus d_3 \oplus d_4$
 $\Rightarrow c_3 = 0 \oplus 1 \oplus 1 \oplus 0 = 0$
- Syndrome = 011 \Rightarrow bit 3 in error = d_1

Syndrome Size

- n data bits m parity bits
- range of $m \geq n + m$
- $2^m - 1 \geq n + m$
- e.g. $m = 4$ can protect a max of 11 data bits

Hamming Code cont.

Syndrome (c4,c3,c2,c1)	Inference
0 0 0 0	no error
0 0 0 1	p1 in error
0 0 1 0	p2 in error
0 0 1 1	d1 in error
0 1 0 0	p3 in error
0 1 0 1	d2 in error
0 1 1 0	d3 in error
0 1 1 1	d4 in error
1 0 0 0	p4 in error
1 0 0 1	d5 in error
1 0 1 0	d6 in error
1 0 1 1	d7 in error
1 1 0 0	d8 in error
1 1 0 1	d9 in error
1 1 1 0	d10 in error
1 1 1 1	d11 in error

Extended Hamming Code

- single bit error correction
- double bit error detection
- e.g. $n = 4$ add an extra parity bit p_4 where

$$p_4 = p_1 \oplus p_2 \oplus d_1 \oplus p_3 \oplus d_2 \oplus d_3 \oplus d_4$$

$$c_4 = p_4 \oplus p_1 \oplus p_2 \oplus d_1 \oplus p_3 \oplus d_2 \oplus d_3 \oplus d_4$$

Syndrome and c_4 Inference

- $c_4 = 0$ and syndrome = 0 \Rightarrow no error
- $c_4 \neq 0$ and syndrome $\neq 0$ \Rightarrow single bit error
(can be corrected)
- $c_4 = 0$ and syndrome $\neq 0$ \Rightarrow double bit error
(can not be corrected, detection only)
- $c_4 \neq 0$ and syndrome = 0 \Rightarrow p_4 in error

Burst Errors

- form Hamming blocks on the "vertical" rather than the "horizontal"

